

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	METHOD AND STRUCTURE FOR PROVIDING TUNED LEAKAGE CURRENT IN CMOS INTEGRATED CIRCUIT
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Application Number :

Confirmation Number:

First Named Applicant: Brent Anderson

Attorney Docket Number: BUR920040019US1

Art Unit:

Examiner:

Search string: (4827324 or 5264380 or 5321283 or 6509233 or 20020123196).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	4827324	1989-05-02	Blanchard			
	2	5264380	1993-11-23	Pfiester			
	3	5321283	1994-06-14	Cogan et al			
	4	6509233	2003-01-21	Chang et al			

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20020123196	2002-09-05	Chang et al			

Signature

Examiner Name	Date